



## LOW POWER ISOLATED RS485 TRANSCEIVER

### FEATURES

- ◆ Isolated RS485 Transceivers
- ◆ 2.5kVrms Isolation
- ◆ 2Mbps or Low EMI 250kbps Data Rate
- ◆ Isolated DC Power up to 1W
- ◆ High ESD:15KV HBM on Transceiver Interface
- ◆ High Common Mode Transient Immunity:30KV/μs
- ◆ Single 5.0V Operation
- ◆ No External Components Required
- ◆ High Input Impedance RS485 Receiver
- ◆ Compatible with TIA/EIA-485-A Specification

### APPLICATIONS

- ◆ Industrial Networks
- ◆ Isolated RS485 Interface
- ◆ Motor Controllers
- ◆ Building Control Systems
- ◆ Distributed Control Systems

### DESCRIPTION

The CMP485S is a complete galvanically isolated half-duplex RS485 transceiver designed for bi-directional data communication or multipoint bus transmission at rates up to 2Mbps per second. No external components are needed. Only a single 5.0V power supply is required on the input side. The both sides of the interface can be powered through the integrated, isolated, low noise, low power dc/dc converter. The internal digital isolators and an isolation power transformer provide 2500Vrms of isolation between the line transceiver and the logic interface. This device is ideal for systems where the ground loop is broken allowing for large common mode voltage variation. Uninterrupted communication is guaranteed for common mode transients greater than 30KV/μs.

Maximum data rates are 2Mbps. Their BiCMOS design allows low power operation without sacrificing performance. Transmit data and receive data are implemented with event driven low jitter processing. The driver of the device has an enable control line which is active HIGH.

Enhanced ESD protection allows this part to withstand up to ±15kV (human body model) on the transceiver interface pins to isolated supplies and ±10kV through the isolation barrier to logic supplies without latch-up or damage. The device is supplied in a low profile 10 pin DIP package.



SELECTION GUIDE							
ORDER CODER	OPERATION VOLTAGE	COMM MODE	TX CHANNEL	RX CHANNEL	Data rate (KBPS)	NODE	ESD(KV)
CMP485L*	5	Half	1	1	10	256	15
CMP485L-3*	3.3	Half	1	1	10	256	15
CMP485H*	5	Half	1	1	250	256	15
CMP485H-3*	3.3	Half	1	1	250	256	15
CMP485LC**	5	Half	1	1	250	256	15
CMP485LC-3**	3.3	Half	1	1	250	256	15
CMP485HC**	5	Half	1	1	250	256	15
CMP485HC-3**	3.3	Half	1	1	250	256	15

\*Controlling end to receive high-level  
 \*\*Controlling end to receive low-level

ABSOLUTE MAXIMUM RATINGS	
PARAMETER	RATING
Vcc to GND	-0.5V to +7V
Receiver differential input voltage rang	±15V
Diver output voltage rang	±15V
Power dissipation	2000mW
Isolation voltage	2500Vrms
Data transmission rate	2Mbps
Storage temperature	-55°C to +150°C
Operation temperature	-40°C to +125°C

RECEIVER FUNCTION TABLE			
Differential inputs	CON	ROUT	
V <sub>ID</sub> ≥ 0.2V	High level	High level	
V <sub>ID</sub> ≤ -0.2V	High level	Low level	
-0.2V ≤ V <sub>ID</sub> ≤ 0.2V	High level	Undefined	



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**ELECTRICAL CHARACTERISTICS**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
<b>Supplies</b>						
Power supply voltage	V <sub>CC</sub>	4.75	5.0	5.25	V	
Isolated output voltage	V <sub>DD</sub>	4.75	5.0	5.25	V	
<b>Logic</b>						
Logic input high level	V <sub>IH</sub>	3.3			V	
Logic input low level	V <sub>IL</sub>			1.5	V	
Logic high output level	V <sub>OH</sub>	V <sub>CC</sub> -0.1	5.0		V	I <sub>o</sub> =-20μA
Logic low output level	V <sub>OL</sub>		0	0.1	V	I <sub>o</sub> =20μA
<b>Driver</b>						
Open circuit voltage	V <sub>OC</sub>			6.0	V	
Differential voltage	V <sub>DIFF</sub>	1.5		5.0	V	R <sub>L</sub> =54Ω, C <sub>L</sub> =50pF
Balance	V <sub>B</sub>			±0.2	V	
Common-mode output	V <sub>COMD</sub>			3.0	V	
Output current	I <sub>DO</sub>	28.0			mA	R <sub>L</sub> =54Ω
Short circuit current	I <sub>SC</sub>			±250	mA	
Output transition time	t <sub>OT</sub>		30		ns	
Propagation delay time	t <sub>PHL</sub>		100	200	ns	R <sub>L</sub> =54Ω, C <sub>L</sub> =50pF
Propagation delay time	t <sub>PLH</sub>		100	200	ns	R <sub>L</sub> =54Ω, C <sub>L</sub> =50pF
Drive output skew	t <sub>OS</sub>		5	15	ns	
<b>Receiver</b>						
Common-mode range	V <sub>COMR</sub>	-7.0		+12	V	
Receiver sensitivity	V <sub>SEN</sub>			±0.2	V	
Input impedance	R <sub>I</sub>	120	150		kΩ	
Propagation delay time	t <sub>PHL</sub>			1200	ns	R <sub>L</sub> =54Ω, C <sub>L</sub> =15pF
Propagation delay time	t <sub>PLH</sub>			1200	ns	R <sub>L</sub> =54Ω, C <sub>L</sub> =15pF
Differential receiver skew	t <sub>RS</sub>		60		ns	

**ELECTRICAL CHARACTERISTICS**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Isolation voltage			2500		V <sub>rms</sub>	Flash tested for 1s
Isolation capacitance			40		pF	

**DRIVER FUNCTION TABLE**

TIN	CON	A	B
High level	High level	High level	Low level
Low level	High level	Low level	Low level
Irrelevant	Low level	High impedance	High impedance

**TEMPERATURE CHARACTERISTICS**

Parameter	Min.	Typ.	Max.	Unit
Operating temperature range	-40		+85	°C
Storage temperature range	-40		+125	°C

## APPLICATION NOTES

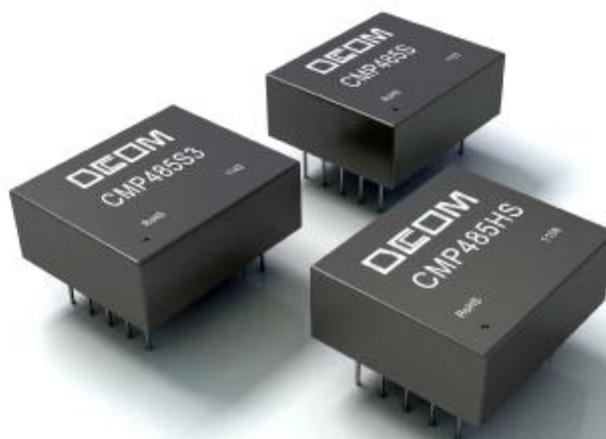
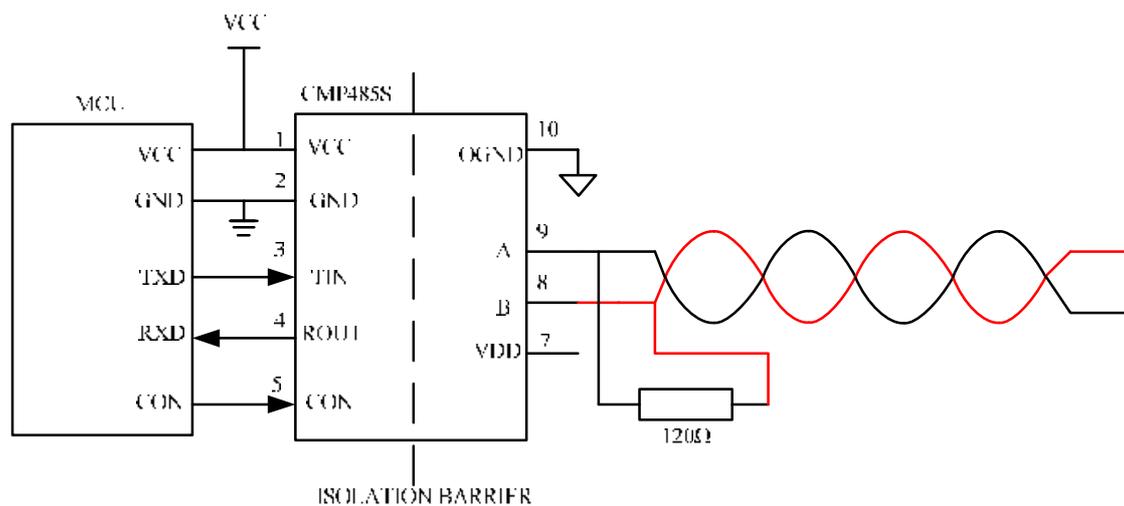
The increased use of balanced data transmission lines, (distributing data to several system components and peripherals over relatively long lines) has brought about the need for multiple driver/receiver combinations on a single twisted pair line. This resulted in an upgraded version of EIA RS-422, named EIA-485. EIA-485 takes into account EIA RS-422 requirements for balanced line data transmission, and allows for multiple drivers and receivers. The CMP485S is a low power isolated differential interface providing EIA-485 compatibility. The use of a differential communications interface such as the CMP485S allows data transmission at high rates and over long distances to be accomplished. This is because effects of external noise sources and cross talk are much less pronounced on the data signal. Any external noise source coupling onto the differential lines will appear as an extra common mode voltage which the receiver is insensitive to. The difference between the signal levels on the two lines will therefore remain the same. Similarly a change in the local ground potential at one end of the line will appear as just another change in the common mode voltage level of the signals. Twisted pair cable is commonly used for differential communications since its twisted nature tends to cause cancellation of the magnetic fields generated by the current flowing through each wire, thus reducing the effective inductance of the pair. Computer and industrial serial interfacing are areas where noise can seriously affect the integrity of data transfer, and a proven route to improve noise performance for any interface system is galvanic isolation. Galvanic isolation removes the ground loop currents from data lines and hence the impressed noise voltage which affects the signal is also eliminated. The isolation feature of the CMP485S also means that common mode noise effects are removed and many forms of radiated noise are reduced to negligible limits.

If two or more CMP485S are used and drivers are shorted directly, the driver outputs can not supply enough current to activate the thermal shutdown. Thus the internal shutdown circuit will not prevent contention faults when two drivers are active on the same bus at the same time.

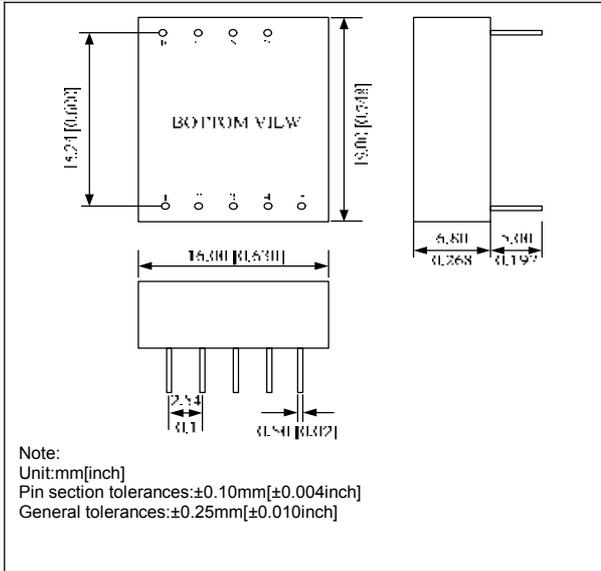
Figure 1 demonstrates how the differential lines of the CMP485S can be connected to form a transceiver. Data direction is controlled by the CON pin. This means the device can receive when the CON is low and transmit when the CON is high. As the driver is active high, to reduce the power dissipation even further, it is advisable to disable the driver when not transmitting data.

## TYPICAL APPLICATIONS

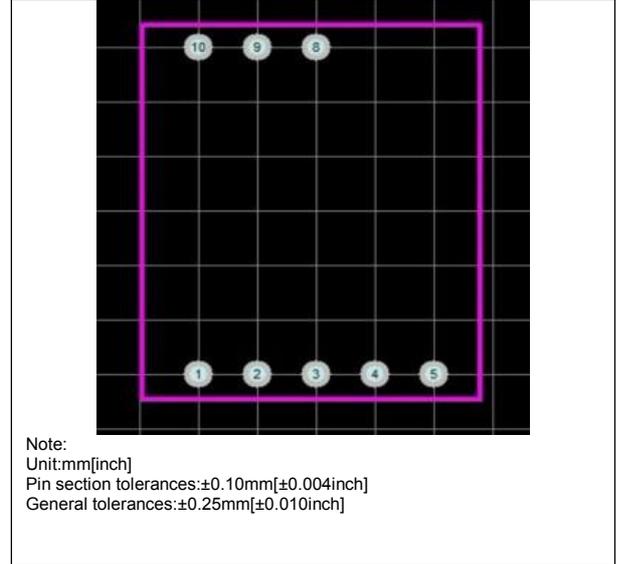
Figure 1



## MECHANICAL DIMENSIONS



## RECOMMENDED FOOTPRINT



## FOOTPRINT DETAILS

Pin	Function	Description
1	VCC	Power supply (logic side)
2	GND	Ground
3	TIN	Driver input data, TTL/CMOS logic
4	ROUT	Receiver output data, TTL/CMOS logic
5	CON	Driver/receiver transform control input
6	NC	No internal connection
7	VDD	Isolated power supply (bus side)
8	B	Inverting Driver Output/Receiver Input
9	A	Non-inverting Driver Output/Receiver Input
10	OGND	Isolated ground

Specifications can be changed any time without notice.



### RoHS COMPLIANT INFORMATION

This series is compatible with RoHS soldering systems with a peak wave solder temperature of 300°C for 10 seconds. The pin termination finish on the SIP package type is Tin Plate, Hot Dipped over Matte Tin with Nickel Preplate. The DIP types are Matte Tin over Nickel Preplate. Both types in this series are backward compatible with Sn/Pb soldering systems.



### REACH COMPLIANT INFORMATION

This series has proven that this product does not contain harmful chemicals, it also has harmful chemical substances through the registration, inspection and approval.